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UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Tyler A. Lowrey et al.

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Art Unit: 2827

Serial No.: 10/633,872

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Examiner: Thong Quoc Le

Filed: August 4, 2003

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Docket: ITO.0046US  
P16201

For: Read Bias Scheme for  
Phase Change Memories

§

Assignee: Intel Corporation

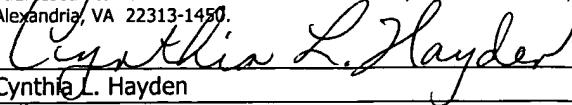
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**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO PAPER NO. 5**

Sir:

In response to the office action mailed February 21, 2006, please amend the above-referenced patent application as follows:

Date of Deposit: <u>May 3, 2006</u>
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as <b>first class mail</b> with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Cynthia L. Hayden